Crash Course Computer Science #6: Registers and RAM

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| Name: |  | Date: |  |

# Instructions

Watch *Crash Course Computer Science #6: Registers and RAM* on YouTube first. Then answer the following questions. Try to answer the question without looking at the video, but re-watch the video or parts of it if you cannot remember the answer.

1. What does “RAM” stand for?

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1. Why do we need RAM at all?

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1. What’s the major disadvantage of RAM, or rather, what’s its limitation? What does it require in order to hold values?

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1. What is a Latch?

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1. What is it called when you set a value in memory? How about when you get it out?

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1. What do we call a memory module that has a way to “lock” the writing of a memory value?
   1. Gross Latch
   2. Gated Latch
   3. Fenced Latch
   4. Latched Latch
2. What’s the term for the line that you have to set in order to write a value to memory?

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1. What do we call a whole bunch of latches in a row to store a binary number?
   1. A register
   2. A rogister
   3. A ram
   4. A gigabyte
2. There’s a way to construct a really large number of memory locations without using too many wires; what’s the name of that construct?

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1. How do we find a location to store or read a memory value? What’s the term called?

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1. What’s the name of the device used to decode memory locations?
   1. A mogrifier
   2. A register
   3. A latch
   4. A multiplexer
2. What’s “SRAM” stand for?

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